

ELECTRONIC INFORMATION DISCLOSURE STATEMENT

Electronic Version v18

Stylesheet Version v18.0

Title of
Invention

CIRCUIT AND METHOD OF ESTABLISHING DC BIAS LEVELS
IN AN RF POWER AMPLIFIER

Application Number: 10/743221

10/743221

Confirmation Number: 7643

First Named Applicant: Alan Westwick

Attorney Docket Number: SIL.P0063

Art Unit: 2817

Search string: (6462620 or 6549071).pn.

US Patent Documents

Note: Applicant is not required to submit a paper copy of cited US Patent Documents

init	Cite.No.	Patent No.	Date	Patentee	Kind	Class	Subclass
	1	6462620	2002-10-08	Dupuis et al.			
	2	6549071	2003-04-15	Paul et al.			

Remarks

Note: Remarks are not for responding to an office action.

Applicants would also like to make the Examiner aware of the following commonly owned co-pending patent application: Serial No. 10/743,217, filed on 12/22/2003, attorney docket number SIL.P0064, entitled "CIRCUIT AND METHOD OF REDUCING NOISE IN AN RF POWER AMPLIFIER", by Alan L. Westwick et al. (pending).

Signature

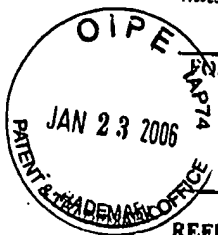
Examiner Name

Date



DUPLICATE

Printed on October 20, 2003



PATENT APPLICATION

Page 1 of 1

2NRM PTO-1449

ATTY. DOCKET NO.

SIL.P0063

SERIAL NO.

10743,221

APPLICANT

Westwick et al.

FILING DATE

12/22/2003

GROUP

2817

REFERENCE DESIGNATION

U.S. PATENT DOCUMENTS

[illegible]

FOREIGN PATENT DOCUMENTS

[illegible]

OTHER REFERENCES (Including Author, Title, Date, Pertinent Pages, etc.)

[illegible]

EXAMINER

DATE CONSIDERED

Rev. 10/94 (Form 3.05)

ELECTRONIC INFORMATION DISCLOSURE STATEMENT

Electronic Version v1.8

Stylesheet Version v1.8.0

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US Published Applications

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Init	Cite.No.	Pub. No.	Date	Applicant	Kind	Class	Subclass
	1	20050134378	2005-06-23	Westwick et al.			
	2	20050134386	2005-06-23	Westwick et al.			
	3	20050134375	2005-06-23	Westwick et al.			

Remarks

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A paper IDS is being submitted on the same date as this electronic IDS.

Signature

Examiner Name	Date